

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC-AUTONOMOUS INSTITUTION)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2024 - Band: 201-300 (Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA
www.mits.ac.in

EXAM SECTION - NOTIFICATION DATE: 05.05.2025

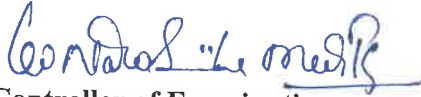
M. Tech I Year II Semester I Mid – Term Test Schedule May 2025

Academic Year – 2024 – 25

Timings: FN: 09:30 AM to 11:30 AM
AN : 03.00 PM to 05.00 PM

DATE / DAY/SESSION		VLSI Design & EMBEDDED Systems
15.05.2025 (Thursday)	FN	CMOS Analog IC Design-24VESP103
	AN	Embedded System Design-24VESP104
16.05.2025 (Friday)	FN	Pattern Recognition and Machine Learning-24VESP407
	AN	Physical Design Automation-24VESP412
17.05.2025 (Saturday)	FN	English for Research Paper Writing-24AUP903

Note: 1. HOD is requested to circulate among the faculty in the department.



Controller of Examinations

Copy to:

1. HOD
2. TI
3. Copy to HOD to be read in M. Tech , I Year II Semester Class Room
4. Notice Board (AS & Dept)
5. File (AS)



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